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**Books Published:**

<b>Sr. No.</b>	<b>Title of the book</b>	<b>Publishing Year</b>	<b>ISBN Number</b>	<b>Name of Publisher</b>
1.	DC Machines and Transformers	First Edition, July 2014	978-93-83058-59-4	Mahajan Publishing House, Ahmedabad
2.	“Elements of Electrical Design”	Forth Edition, July-2013	978-93-83058-10-5	Mahajan Publishing House, Ahmedabad
		Third Edition, July-2011	978-93-81256-04-6	
3.	“Switchgear”	First Edition, July-2012	978-93-81256-59-6	Mahajan Publishing House, Ahmedabad
4.	Electrical Machines-I	First Edition, July-2013	978-93-83058-12-9	Mahajan Publishing House, Ahmedabad

**Publications:**

**List of Papers Published/Presented in National / International conferences/ Journals: 30**

**RESEARCH PUBLICATIONS**

1. J.G.Jamnani & S.A.Kanitkar, “TRV Rating Concepts and Generation of TRV Envelopes for Synthetic Testing of Extra High Voltage Circuit Breakers” International Journal of Electrical and Computer Engineering (IJCEE). Vol.3, No.1, pp.24-29, Feb. 2011
2. J.G.Jamnani & S.A.Kanitkar, “Computer Aided Optimized Design and Simulation of Synthetic Test Circuit for Testing 800kV Rating Circuit Breakers” IEEE International conference on Emerging Technologies for sustainable development (TENCON2009) held **at Singapore** during November 23-26,2009
3. J.G.Jamnani & S.A.Kanitkar, “Computer Aided Optimized Design, Simulation and Comparison of Synthetic Test Circuits for Testing 420 kV Rating Circuit Breakers” IEEE PES Power systems International conference and Exposition (PSCE-09) held during March 15-18, 2009 **at Seattle, Washington , USA.**

4. J.G. Jamnani & S. A. Kanitkar, "Design, Simulation and Comparison of Synthetic test circuits for High Voltage Circuit Breakers" International Conference on power system Technology 2008 (POWERCON 2008) & 2008 IEEE Power India Conference held during October 12-15, 2008 at New Delhi.
5. J.G.Jamnani & S.A.Kanitkar , "Computer Aided Optimized Design, Simulation and Comparison of Synthetic Test Circuits For 245kV Rating Circuit Breakers" International Journal of Engineering and Technology , IETECH Journal of Electrical Analysis, Vol.2, No.4, pp.258-262, Nov. 2008.
6. J.G. Jamnani & S.A.Kanitkar , "Development and Fabrication of Automatic Controller and Triggering Circuit for Circuit Breakers Synthetic Test Circuit" at 15<sup>th</sup> National Power System Conference ( NPSC-2008) held at IIT , Bombay during Dec.16-18 2008, pp.37-42.
7. J. G. Jamnani & S.A.Kanitkar, "Design, Simulation and Comparison of Synthetic test circuits for Extra High Breakers" IET-International Conference on Information and communication Technology in Electrical Sciences (ICTES-2007) held during Dec. 20 -22, 2007 at Chennai, pp.464-468.
8. J.G. Jamnani & S.A.Kanitkar "Design and Simulation of 2- Parameters TRV synthetic testing circuits for Medium voltage circuit breakers" IEEE International Conference on Electrical Engineering, (ICECE-2006) held during Dec.19-21, 2006 at **Dhaka, Bangladesh**, pp.1-4.
9. J.G. Jamnani & S.A.Kanitkar , "Design and Simulation of 4 - Parameters TRV synthetic testing circuits for High Voltage circuit breakers" IEEE International Conference on Electrical Engineering (ICECE-2006) held during Dec.19-21, 2006 at **Dhaka, Bangladesh**, pp. 25-28.
10. J.G. Jamnani & S.A.Kanitkar, " Review of synthetic testing circuits for medium and high voltage circuit breakers" at National conference on power system Engg. PSE- 06, Sri Ramakrishna Engg.College, Coimbatore, Nov. 2006, pp.3 -12.
11. J.G. Jamnani & S.A.Kanitkar, "Analysis and computer aided design of synthetic testing circuits for high voltage circuit breakers" at National conference on current trends in technology , NUCONE-2006, Institute of Technology, Nirma University , Ahmedabad, pp.235-238,Dec.2006.

## **OTHER PUBLICATIONS**

12. Avdhut D. & J.G.Jamnani, "Closed loop control of Thyristor switched capacitor for instantaneous reactive power compensation" at International Journal of Engineering development and Research, pp. 84-87, January, 2014.
13. Nikunj Patel & J.G. Jamnani, " Solid state ON load Tap Changer for Transformer using Micro-controller" at National conference on Recent Trends in Electrical and Electronics Engineering, ITM Universe, Vadodara, January 2014, pp. 101-104.
14. J. G. Jamnani & Kaustubh A. Vyas, "Development of IEEE Complaint Software 'Economical Substation Grounding System Designer' Using MATLAB GUI Development Environment" International Journal on Electrical Engineering and Informatics, Volume 4, Number 2, July 2012
15. Kaustubh A. Vyas & J. G. Jamnani, "Optimal design of grounding system for HV/EHV substations in Two Layered Soil", at International Journal of Emerging Technology and Advanced Engineering (IJETAE), Vol. 2, Issue 5, pp. 383-392, May 2012
16. Kaustubh A. Vyas & J. G. Jamnani, "MATLAB GUI Development Environment Simplifies Deployment of Standalone Software for Optimal design of Substation Grounding Systems", at International conference on Computer Science and Information Technology, ICCSIT-2012, Pune, April 2012, pp. 303-311.
17. J. V. Kadia & J. G. Jamnani, "Modelling and Analysis of TCSC Controller for Enhancement of Transmission Network", at International Journal of Emerging Technology and Advanced Engineering (IJETAE), Vol. 2, Issue 3, pp. 1-7, March 2012
18. Kaustubh A. Vyas & J.G.Jamnani, "Optimal design and development of software for design of substation grounding system" at International conference on current trends in technology, NUiCONE-2011, Institute of Technology, Nirma University, Ahmedabad, Dec. 2011, pp.1-6
19. Ishan Desai & J.G.Jamnani, "Reliability Improvement of Extra high voltage substation" at International conference on current trends in technology, NUiCONE-2011, Institute of Technology, Nirma University, Ahmedabad, Dec. 2011.
20. Avdhut D. & J.G.Jamnani, "Maintaining voltage profile in a power system using shunt connected FACTs controllers" at International conference on Innovative Science and Engg. Technology ICISSET-2011, V.V.P Engineering College, Rajkot, April, 2011.

21. Avdhut D. & J.G.Jamnani, "Comparative analysis of series connected FACTS controllers with conventional methods of Series compensation for large industrial loads" at International conference on current trends in technology, NUiCONE-2010, Institute of Technology, Nirma University, Ahmedabad, Dec. 2010.
22. Jignesh Pandya & J.G.Jamnani, "A 10kV, 100mA DC Power Supply using Zero Voltage Switching with Full Bridge AC-DC Converter" at International conference on current trends in technology, NUiCONE-2010, Institute of Technology, Nirma University, Ahmedabad, Dec. 2010.
23. Hiren Lakhani & J.G. Jamnani, "Review of different topologies of firing of series SCR at medium voltage" at National conference on current trends in technology, NUCONE-2008, Institute of Technology, Nirma University, Ahmedabad, Nov.2008, pp. 244-249.
24. SwapnilJani & J.G. Jamnani, "Simulation and development of Excitation controller for synchronous motor" at National conference on current trends in technology, NUCONE-2008, Institute of Technology, Nirma University, Ahmedabad, Nov.2008, pp. 256-259.
25. H. S. Pandya & J. G. Jamnani "Computer Aided Optimized Design of PMBLDC Motors" at National conference on current trends in technology, NUCONE-2006, Institute of Technology, Nirma University, Ahmedabad, Dec.2006, pp. 214-217.
26. J.G. Jamnani, "CAD and hardware implementation of 2-parameters TRV synthetic testing circuits for high voltage circuit breakers" at IEE - UK, International conference on Energy, IT and power sector, PEITSICON-2005, Calcutta Branch, Kolkata, Jan.2005, pp.552-556.
27. J.G. Jamnani, "Computer Aided Design and development of Synthetic Testing Unit for High voltage CBs with Automatic Electronic Control Circuit" at International Conference on Emerging Technology, ICET- 2003 held at Kalinga Institute of Industrial Technology, Bhubaneswar, Dec. 2003.
28. J.G. Jamnani, "Quality of power: A challenge to power engineers" Journal of Engineering & Technology, Sardar Patel University, VallabhVidyanagar, December 2004, pp.62-67.
29. J.G. Jamnani, "Development of 5kV, 8mA, 20 KHz power supply using fly back transformer for pulse power application" at National conference on Recent Advances in Electrical Engg. EAR-2004 held at JNTU College of Engg., Anantapur (AP), pp.17-23.

30. R. B. Jadeja & J.G. Jamnani, "Modelling and simulation of solid state switch for plasma discharge experiments" at 19<sup>th</sup> National Symposium on plasma and Technology, PLASMA 2004, Jhansi, pp.115-119.